

Circuits logiques et numériques

Lecturer

Dragomir MILOJEVIC (Coordinator)

Course mnemonic

ELEC-H305

ECTS credits

5 credits

Language(s) of instruction

French

Course period

First term

Course content

Boolean Algebra. Optimization of logic functions (K-Maps, Quine Mac-Cluskey). Synthesis of the combinatorial and sequential asynchronous and synchronous Logic Circuits.

Pre-requisites and co-requisites

Co-requisites courses

ELEC-H2001 | Electromagnétisme | 5 crédits

Teaching method and learning activities

References, bibliography and recommended reading

Principle of Digital Design, Daniel D. Gajski (Trentia Hall)
Introduction to Logic Design (2nd Edition), Sajjan G. Shiva (Marcel Dekker)

Other information

Contact(s)

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Evaluation method(s)

Other

Programmes

Programmes proposing this course at the
Brussels School of Engineering

BA-IRCI | Bachelor in Engineering Sciences | option Bruxelles/unit 3